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Jc857 U.S. PTO
09/927295
08/10/01

Class | Subclass | ISSUE CLASSIFICATION

P

PATENT NUMBER

U.S. UTILITY Patent Application

JC#6
JC858

O.I.P.E.	PATENT DATE
SCANNED KS TR2 Q.A. LD	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/927295	D	451	41	3723	W. Berry DTN

APPLICANTS: Scott Moore

TITLE: Method and apparatus for planarizing a microelectronic substrate with a tilted planarizing surface

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____

☐ The terminal _____ months of this patent have been disclaimed.

DRAWINGS		
Sheets Drwg.	Figs. Drwg.	Print Fig.

(Assistant Examiner) (Date)

(Primary Examiner) (Date)

(Legal Instruments Examiner) (Date)

CLAIMS ALLOWED	
Total Claims	Print Claim for O.G.

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due	Date Paid

ISSUE BATCH NUMBER

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